

YO999-153DIV

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re divisional patent application of 09/325,732 filed on June 4, 1999

Chen, et al.

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For: METHOD FOR FABRICATING COMPLEMENTARY METAL OXIDE
SEMICONDUCTOR (CMOS) DEVICES ON A MIXED BULK AND SILICON-ON-
INSULATOR (SOI) SUBSTRATE

Assistant Commissioner of Patents
Washington, D.C. 20231

SUBMISSION OF PROPOSED DRAWING CORRECTIONS

Sir:

Submitted herewith is a corrected formal drawing for Figure 6 which has been approved in the parent application. Please substitute the attached drawing Figure for the original drawing Figure which was filed with the application.

Respectfully submitted,



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